CPAN Network on Instrumentation and Detectors (CNID)

Thursday 09 May 2024

Electronics (ASIC, DAQ, Trigger, powering) WG - Salón de Actos (8:55-11:15)

 -Moderadores: Diego Real Mañez; Fernando Arteche; Santiago Folgueras

time	[id] title	presenter
9:00	[43] ITA activities in DRD7: Power efficiency & Timing	Dr. ARTECHE, Fernando
9:15	[8] Triggering on muon showers in the Barrel Muon Trigger of the CMS experiment for the HL-LHC upgrades	PRADO PICO, Javier
9:30	[38] Research and development of the acquisition electronics for the KM3NeT neutrino telescope	Dr. REAL MAÑEZ, Diego
9:45	[9] Enhancing Data Processing in the CMS Trigger System for HL-LHC with HLS	LEGUINA, Pelayo
10:00	[11] Programmable readout electronics for radiation environments	PAZ HERRERA, Rolando
10:15	[28] The Energy Measurement ASIC for the Upgrade II in the LHCb Calorimeter Detector	LÓPEZ GUILLÉN, Alberto
10:30	[39] Precise & deterministic timing distribution study with Microsemi FPGA.	Dr. SASTRE ÁLVARO, Javier Dr. REDONDO FERNANDEZ, Ignacio
10:45	[19] Development of an AI-based Central Trigger Processor board for the advanced SiPM based camera of the CTA Large Size Telescopes	PÉREZ AGUILERA, Alejandro
11:00	[14] The PreProcessor system for the ATLAS Tile Hadronic Calorimeter at the HL-LHC	VALERO, Alberto