

Activities

- ▶ **Main devices:**

- ▶ SNAP12 module (High-Speed Link up to 75 Gbps).
- ▶ SFP module (High-Speed Link up to 1 Gbps).
- ▶ Stratix II GX.

- ▶ **Physical card features:**

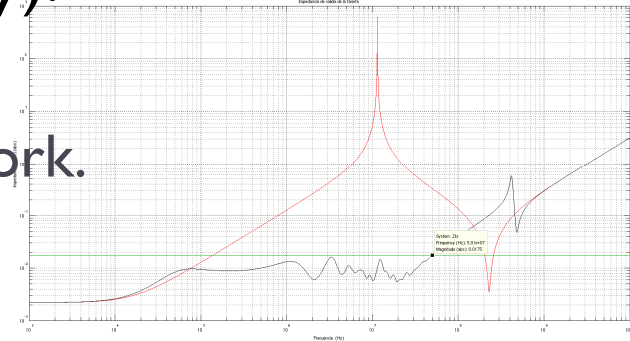
- ▶ Size: 2 x PU position in ROD (174mm x 120mm).
- ▶ 12 layers.
- ▶ 3.3V power supply.



Activities

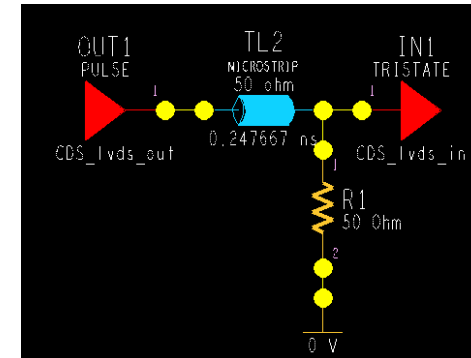
- ▶ **Current phase (3 weeks – 7th January):**

- ▶ PCB routing.
- ▶ Recalculating Power Distribution Network.



- ▶ **Next Phase(1 week – 14th January):**

- ▶ Signal integrity post-layout studies.
 - ▶ Clock lines
 - ▶ High-Speed lines (SNAPI2 & SFP modules)



Nombre	Duración	Inicio	Terminado
1 PCB Routing	21 days	17/12/09 8:00	7/01/10 8:00
2 Recalculating PDN	7 days	17/12/09 8:00	24/12/09 8:00
3 Signal integrity studies	7 days	7/01/10 8:00	14/01/10 8:00
4 End	0 days	14/01/10 8:00	14/01/10 8:00

A Gantt chart showing the duration of each activity on a calendar grid. The x-axis shows dates from 14 dic 09 to 8 feb 10. The y-axis lists activities 1 through 4. Activity 1 (red bar) runs from 17/12/09 to 7/01/10. Activity 2 (blue bar) runs from 17/12/09 to 24/12/09. Activity 3 (red bar) runs from 7/01/10 to 14/01/10. Activity 4 (black bar) is a single point at 14/01/10.

Activities

► Power test board results:

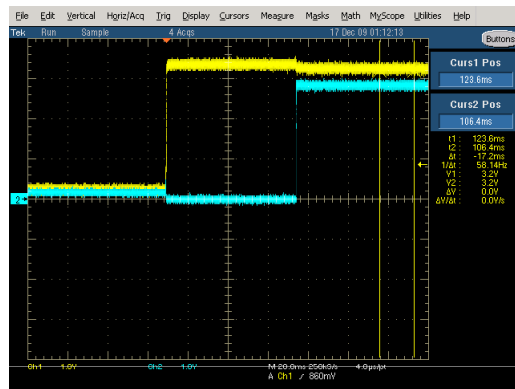


Fig 1. Power Supply & Enable

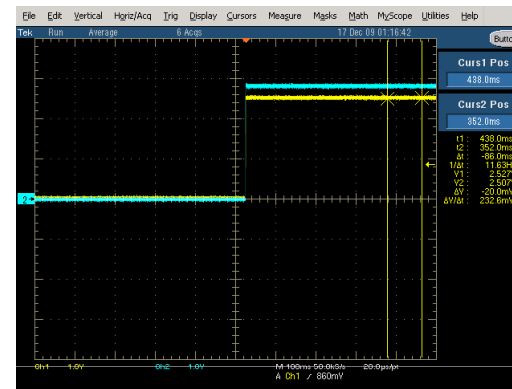


Fig 2. 2.5V & Enable

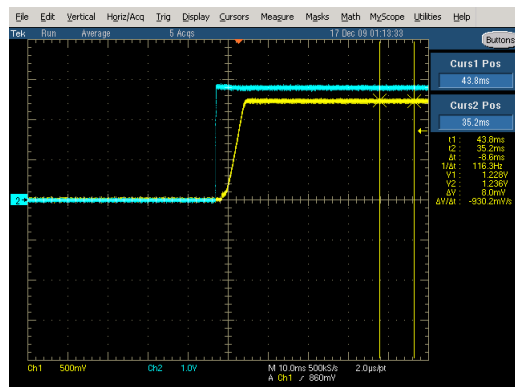


Fig 3. 1.2V & Enable

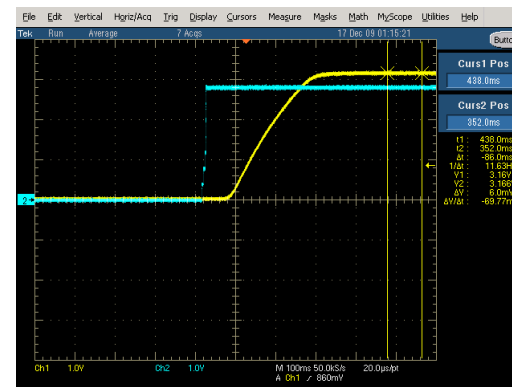


Fig 4. 3.3V (Soft-Start) & Enable