



The artificial retina for track reconstruction at the LHC crossing rate

A. Abba^d, F. Bedeschi^c, M. Citterio^d, F. Caponio^d, A. Cusimano^d, A. Geraci^d, P. Marino^{b,c,*}, M. J. Morello^{b,c},
N. Neri^d, G. Punzi^{a,c}, A. Piucci^a, L. Ristori^{c,e}, F. Spinella^c, S. Stracka^{b,c}, D. Tonelli^f

^aUniversity of Pisa, Lungarno Pacinotti 43, 56126, Pisa, Italy

^bScuola Normale Superiore, Piazza dei Cavalieri 7, 56127, Pisa, Italy

^cINFN-Pisa, L.go Bruno Pontecorvo 3, 56127, Pisa, Italy

^dPolitecnico and INFN-Milano, Via Celoria 16, 20133, Milano, Italy

^eFermilab, Wilson and Kirk Rd, Batavia, IL 60510, USA

^fCERN 385 Route de Meyrin, Geneva, Switzerland

Abstract

We present the results of an R&D study for a specialized processor capable of precisely reconstructing events with hundreds of charged-particle tracks in pixel and silicon strip detectors at 40 MHz, thus suitable for processing LHC events at the full crossing frequency. For this purpose we design and test a massively parallel pattern-recognition algorithm, inspired to the current understanding of the mechanisms adopted by the primary visual cortex of mammals in the early stages of visual-information processing. The detailed geometry and charged-particle's activity of a large tracking detector are simulated and used to assess the performance of the artificial retina algorithm. We find that high-quality tracking in large detectors is possible with sub-microsecond latencies when the algorithm is implemented in modern, high-speed, high-bandwidth FPGA devices.

Keywords: Pattern recognition, Trigger algorithms

1. Introduction

Higher LHC energy and luminosity increase the challenge of data acquisition and event reconstruction in the LHC experiments. The large number of interactions for bunch crossing (pile-up) greatly reduces the discriminating power of usual signatures, such as the high transverse momentum of leptons or the high transverse missing energy. Therefore real-time track reconstruction could prove crucial to quickly select potentially interesting events for higher level of processing. Performing such a task at the LHC crossing rate is a major challenge because of the large combinatorial and the size of the associated information flow and requires unprecedented massively parallel pattern-recognition algorithms. For this purpose we design and test a neurobiology-inspired

pattern-recognition algorithm well suited for such a scope: the *artificial retina* algorithm.

2. An artificial retina algorithm

The original idea of an artificial retina tracking algorithm was inspired by the mechanism of visual receptive fields in the mammals eye [1]. Experimental studies have shown neurons tuned to recognize a specific shape on specific region of the retina (“receptive field”) The strength of the response of each neuron to a stimulus is proportional to how close the shape of the stimulus is to the shape for which the neuron is tuned to. All neurons react to a stimulus, each with different strength, and the brain obtains a precise information of the received stimulus performing some sort of interpolation between the responses of neurons.

The retina concepts can be geared toward track reconstruction. Assuming a generic tracking detector, the

*Corresponding author.

Email address: pietro.marino@pi.infn.it (P. Marino)

33 3D charged particle trajectory is described by five pa-
 34 rameter. The space of track parameters are discretized
 35 into *cells*, which mimic the receptive fields of the retina.
 36 The center of each cell identifies a track in the detec-
 37 tor space, that intersects detector layers in spatial points
 38 that we call *receptors*. For each incoming hit, the al-
 39 gorithm computes the excitation intensity, *i. e.* the re-
 40 sponse of the receptive field, of each cell as follows:

$$41 \quad R = \sum_{k,r} \exp\left(-\frac{s_{kr}^2}{2\sigma^2}\right), \quad (1)$$

42 where s_{kr} is the distance, on the layer k , between the hit
 43 and the receptor r . σ is a parameter of the retina algo-
 44 rithm, that can be adjusted to optimize the sharpness of
 45 the response of the receptors.

46 After all hits are processed, tracks are identified as
 47 local maxima over a threshold in the space of track pa-
 48 rameters. Averaging over nearby cells of the identified
 49 maximum provides track parameters with a significant
 50 better resolution than the available cell granularity.

51 3. Retina algorithm in a real HEP experiment

52 To evaluate the performances and the robustness of
 53 the algorithm in a real HEP detector, we focus on the
 54 upgraded LHCb detector. The upgraded LHCb detec-
 55 tor [2], a single-arm spectrometer covering the pseudo-
 56 rapidity range $2 < \eta < 5$, is a major upgrade of the
 57 current LHCb experiment, and it will run at the instan-
 58 tantaneous luminosity of $3 \times 10^{33} \text{ cm}^{-2} \text{ s}^{-1}$, with a beam en-
 59 ergy of 7 TeV. All the sub-detectors will be read out at
 60 40 MHz, allowing a complete event reconstruction at the
 61 LHC crossing rate. To benchmark the retina algorithm,
 62 we decided to perform the first stage of the upgraded
 63 LHCb detector tracking reconstruction [3], using the in-
 64 formation of only two sub-detectors, placed upstream of
 65 the magnet: the vertex locator (VELO), a silicon-pixel
 66 detector [4] and the upstream tracker (UT) [5], a sili-
 67 con microstrip detector. We used the last eight forward
 68 pixel layers of the VELO and the two axial layers of the
 69 UT. We arbitrarily chose to parametrize tracks with the
 70 following parameters: (u, v, d, z_0, k) . (u, v) are the
 71 spatial coordinate of the intersection point of the track
 72 with a “virtual plane” perpendicular to the z -axis, placed
 73 to a distance z_{vp} from the origin of the coordinate sys-
 74 tem. d is the signed transverse impact parameter, z_0 is
 75 the z -coordinate of the point of the closest approach to
 76 the z -axis. k is the signed curvature in the bending plane
 77 ($\vec{B} = B\hat{y}$).

78 The detector geometry and magnetic field (negligi-
 79 ble in the VELO and about 0.05 T in the UT), allow

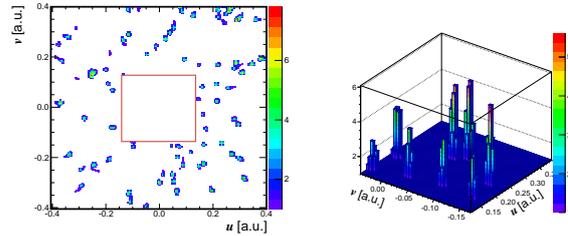


Figure 1: Left: response of the retina algorithm (only the (u, v) -plane, where the pattern recognition is made) to a generic collision from the default LHCb simulation, with instantaneous luminosity of $L = 2 \times 10^{33} \text{ cm}^{-2} \text{ s}^{-1}$. The hole at the center of the figure is due to the physical hole in the VELO layers. Right: a zoom of the retina response.

80 us to use only the (u, v) parameters to perform the pat-
 81 tern recognition, since the 5D tracks’ parameters space
 82 can be factorized into $(u, v) \otimes (d, z_0, k)$. Thus (u, v) are
 83 the “main” parameters where pattern recognition is per-
 84 formed, whereas (d, z_0, k) are treated as “perturbation”
 85 of the main parameters (u, v) [6, 7].

86 To evaluate the performances of the algorithm, we
 87 develop a detailed C++ simulation of the retina algo-
 88 rithm [8] able to process simulated events, interfaced
 89 with the default LHCb simulation. We discretize the
 90 main (u, v) -subspace into 22 500 cells, a granularity
 91 $O(100)$ larger than the maximum expected number of
 92 tracks in a typical upgraded LHCb event. Generic col-
 93 lisions samples from the default LHCb simulation are
 94 used to assess the performances of the retina algorithm.
 95 The generic collisions are generated with beam energy
 96 of 7 TeV, and luminosities up to $L = 3 \times 10^{33} \text{ cm}^{-2} \text{ s}^{-1}$.
 97 A typical response of the retina algorithm is shown in
 98 fig. 1, where several clusters are clearly identifiable, and
 99 most of them reconstructed as tracks.

100 All hits from simulated events from the default LHCb
 101 simulation are sent and processed by the retina. In order
 102 to evaluate tracking performances we considered only
 103 tracks in a region of the (u, v) -plane where they have
 104 full acceptance on the chosen layer configuration. In
 105 addition, cuts close to the ones applied to calculate the
 106 offline efficiency [3] are applied. For instance, we re-
 107 quired at least three hits on VELO layers and two hits
 108 on UT layers, and also a momentum $p > 3 \text{ GeV}/c$
 109 and a transverse momentum $p_T > 200 \text{ MeV}/c$. Tracks
 110 satisfying all these requirements are defined as *recon-*
 111 *structurable*, and the tracking efficiency is defined as the
 112 number of reconstructed tracks over the number of re-
 113 constructable tracks. The efficiency of the retina is re-
 114 ported in figure 2 as function of p_T, d parameters. We
 115 also report the efficiency of the offline LHCb track re-
 116 construction algorithm, performing the same task as the

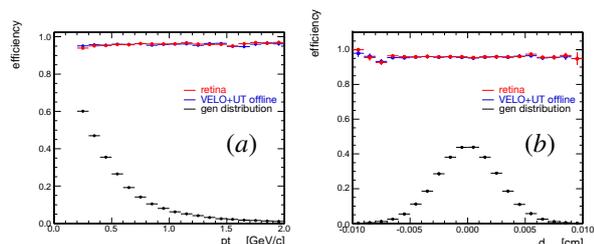


Figure 2: Tracking reconstruction efficiency of the retina algorithm (in red) and of the offline VELO+UT algorithm (in blue), as function of: (a) p_T , (b) d . The distribution of the considered parameter is, also, reported in black. Luminosity of $L = 3 \times 10^{33} \text{ cm}^{-2}\text{s}^{-1}$.

117 retina [6]. The retina algorithm shows very high effi-
 118 ciencies in reconstructing tracks, about 95% for generic
 119 tracks, which is comparable to the offline tracking algo-
 120 rithm. The fake track rate is 8% at $L = 2 \times 10^{33} \text{ cm}^{-2}\text{s}^{-1}$
 121 and 12% at $L = 3 \times 10^{33} \text{ cm}^{-2}\text{s}^{-1}$, slightly higher than
 122 the fake rate of the offline algorithm. We also estimate
 123 the efficiency of the retina algorithm in recostrucing
 124 signal tracks from some benchmark decay modes, such
 125 as $B_s^0 \rightarrow \phi\phi$, $D^{*\pm} \rightarrow D^0\pi^\pm$ and $B^0 \rightarrow K^*\mu\mu$ for
 126 $L = 2 \times 10^{33} \text{ cm}^{-2}\text{s}^{-1}$. The efficiency for these channels
 127 is about 97–98%. Resolutions on tracking parameters
 128 determined by the retina are comparable with those of
 129 the offline reconstruction.

130 4. Hardware implementation

131 To fully exploit the high-grade of parallelism of
 132 the algorithm, we developed the retina algorithm into
 133 FPGA chips [9]. The logic is implemented in VHDL
 134 language; detailed logic-gate placement and simulation
 135 on the high-bandwidth Altera Stratix V device model
 136 5SGXEA7N2F45C2ES is achieved using Altera’s prop-
 137 rietary software. Figure 3 shows an overview of the de-
 138 vices architettura. To achieve an efficient distribution of
 139 the hit information coming from the detector layers to
 140 the cells of the space of track parameters, we design
 141 an intelligent information delivery system that routes
 142 each hit in parallel to all and only those cells for which
 143 such hit is likely to contribute a significant weight. The
 144 switching network completes its processing in 30 clock
 145 cycles. Each cell in the tracks parameter space is de-
 146 fined as a logic module, the *engine*. The engine is im-
 147 plemented as a clocked pipeline, that calculate the ex-
 148 citations. The engine process takes 17 clock cycles. At
 149 the end, the logic that identifies the center-of-mass in the
 150 space of track parameters take 11 cycle of clock cycles
 151 along with another 10 cycles for fanout. With a clock
 152 frequencies of 350 MHz, the latency for reconstructing

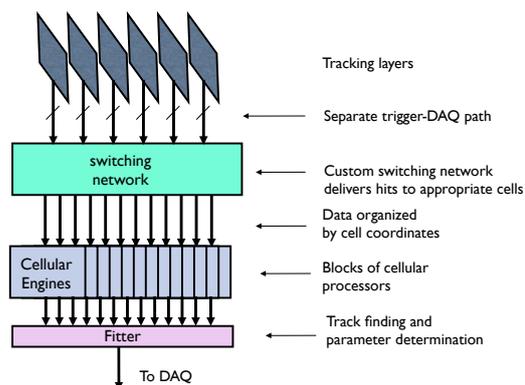


Figure 3: Illustration of the devices architettura.

153 online tracks is less than $0.5 \mu\text{s}$. Each Stratix V can host
 154 up to 900 engines leaving approximately 25% of logic
 155 available for other uses, including a 15% of switching
 156 and the logic for center-of-mass calculation [6].

157 5. Conclusions

158 We showed that high-quality tracking in large LHC
 159 detectors is possible at a 40 MHz event rate with sub-
 160 μs latencies, when appropriate parallel algorithms are
 161 used in conjunction with current high-end FPGA de-
 162 vice. This opens the interesting possibility of designing
 163 high-rate experiments where track reconstruction hap-
 164 pen transparently as part of the detector readout.

165 References

- 166 [1] L. Ristori, An artificial retina for fast track finding, Nu-
 167 clear Instruments and Methods 453 (1-2) (2000) 425 – 429.
 168 doi:http://dx.doi.org/10.1016/S0168-9002(00)00676-8.
 169 [2] LHCb Collaboration, Framework TDR for the LHCb Upgrade:
 170 Technical Design Report, Tech. Rep. LHCb-TDR-12, CERN,
 171 Geneva (Apr 2012).
 172 [3] Bowen E., Storaci B., VeloUT tracking for the LHCb Upgrade,
 173 Tech. Rep. CERN-LHCb-PUB-2013-023, CERN, Geneva (Apr
 174 2014).
 175 [4] LHCb Collaboration, LHCb VELO Upgrade Technical De-
 176 sign Report, Tech. Rep. LHCb-TDR-013, CERN, Geneva (Nov
 177 2013).
 178 [5] LHCb Collaboration, LHCb Tracker Upgrade Technical Design
 179 Report, Tech. Rep. LHCb-TDR-015, CERN, Geneva (Feb 2014).
 180 [6] A. Abba *et al.*, A specialized track processor for the LHCb
 181 upgrade, Tech. Rep. LHCb-PUB-2014-026. CERN-LHCb-PUB-
 182 2014-026, CERN, Geneva (Mar 2014).
 183 [7] A. Abba *et al.*, A specialized processor for track reconstruction
 184 at the LHC crossing rate, Journal of Instrumentation 9 (09) (2014)
 185 C09001.
 186 [8] P. Marino *et al.*, Simulation and performance of an artificial retina
 187 for 40 MHz track reconstruction (2014). arXiv:1409.0898.
 188 [9] D. Tonelli *et al.*, The artificial retina processor for track recon-
 189 struction at the LHC crossing rate (2014). arXiv:1409.1565.