

Activities

▶ Goals:

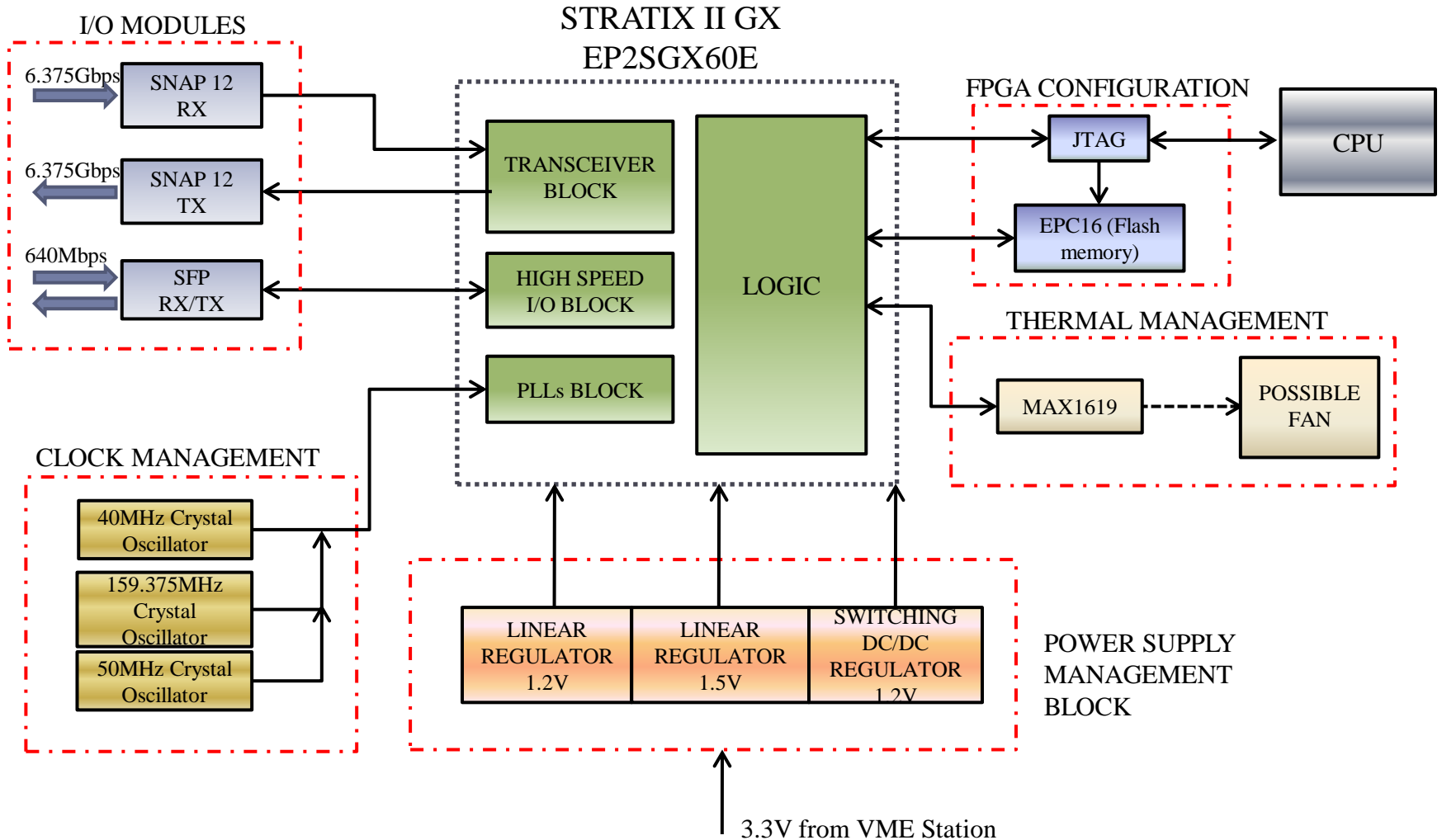
- ▶ High-Speed Link: 75 Gbps with SNAP12 modules (12 fiber optic channels)
- ▶ Size: Process Unit position in ROD

▶ Board Components:

- ▶ Stratix II GX - EP2SGX60E
- ▶ SNAP12 receiver/transmitter (up to 12 x 6.25 Gbps per channel)
 - ▶ SN-T12-C01001 – Reflex Photonics
- ▶ SFP (up to 2.1 Gbps)
 - ▶ V23818-M305-B57 – Infineon
- ▶ JTAG and EPC16 flash memory
- ▶ Thermal management - Possible FAN



Activities



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- ▶ **Hardware Design Phase I (3 weeks):**
 - ▶ Request specifications – 75Gbps
 - ▶ Study of hardware – Stratix II GX, SNAP12
 - ▶ Electronic hardware design: Stratix II GX, Power Supply, ...
- ▶ **Hardware Design Phase II (8 weeks):**
 - ▶ PCB Design
 - ▶ Signal Integrity simulations
 - ▶ PCB manufacturing
- ▶ **Software Design Phase I (3 weeks):**
 - ▶ Programming GX transceivers in Demo Board (1xSFP)
 - ▶ AudioVideo Development Kit (Stratix II GX Edition)
- ▶ **Software Design Phase II (3 weeks):**
 - ▶ Programming board (2xSNAP12 + 1xSFP)
- ▶ **Test (1,5 weeks)**



Activities

	Nombre	Duración	Inicio	Terminado	ago 09							sep 09							oct 09							nov 09							dic 09						
					13	20	27	03	10	17	24	31	07	14	21	28	05	12	19	26	02	09	16	23	30	07													
1	Hardware Design I	15 days	13/07/09 8:00	31/07/09 17:00	[Red bar from 13/07 to 27/07]																																		
2	Hardware Design II	40 days	3/08/09 8:00	25/09/09 17:00								[Red bar from 03/08 to 21/09]																											
3	Software Design I	15 days	28/09/09 8:00	16/10/09 17:00															[Red bar from 28/09 to 12/10]																				
4	Software Design II	15 days	19/10/09 8:00	6/11/09 17:00																						[Red bar from 19/10 to 06/11]													
5	Test	5 days	9/11/09 8:00	13/11/09 17:00																						[Red bar from 09/11 to 13/11]													

Remaining activities(%)

